

FIG. 1

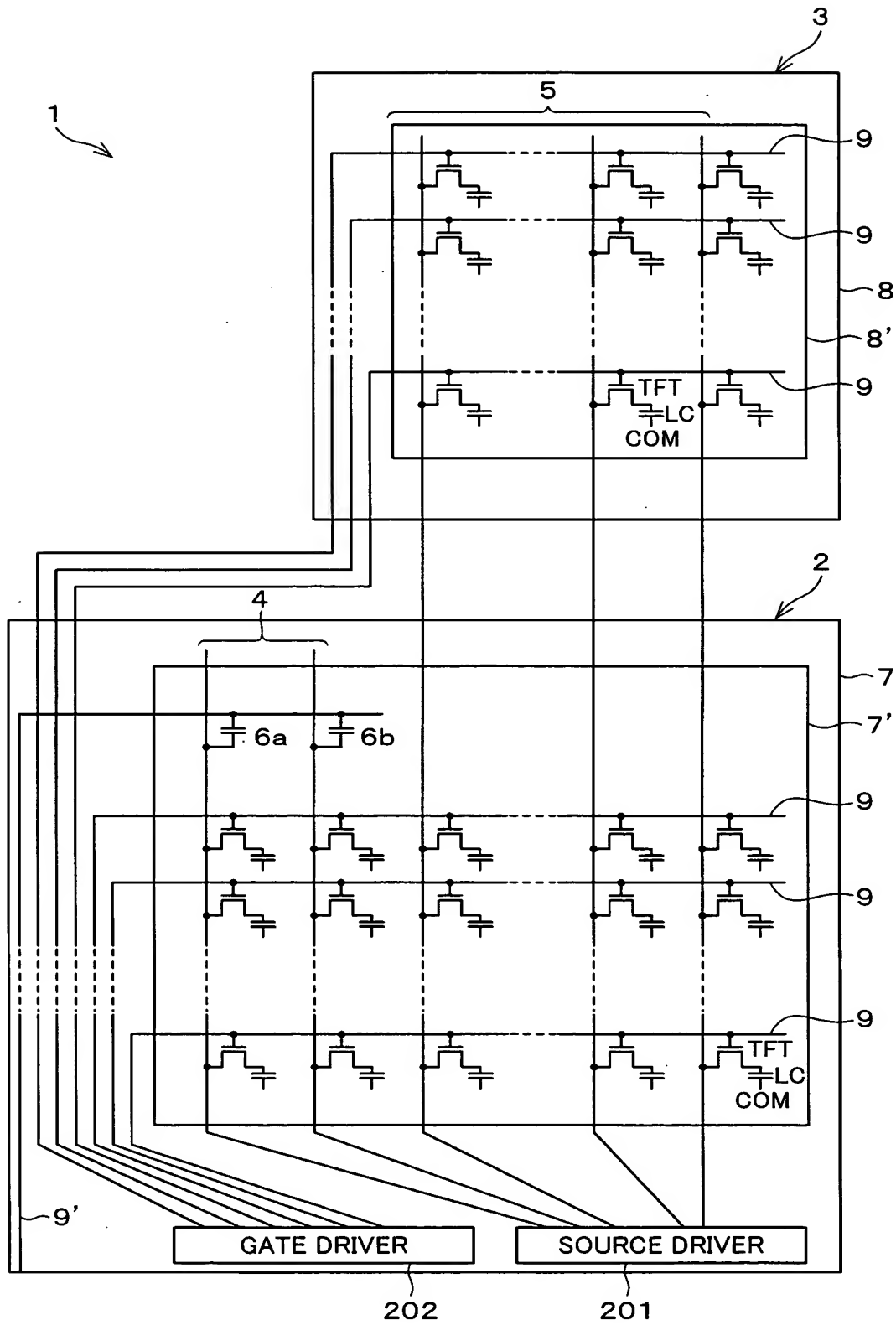


FIG. 2

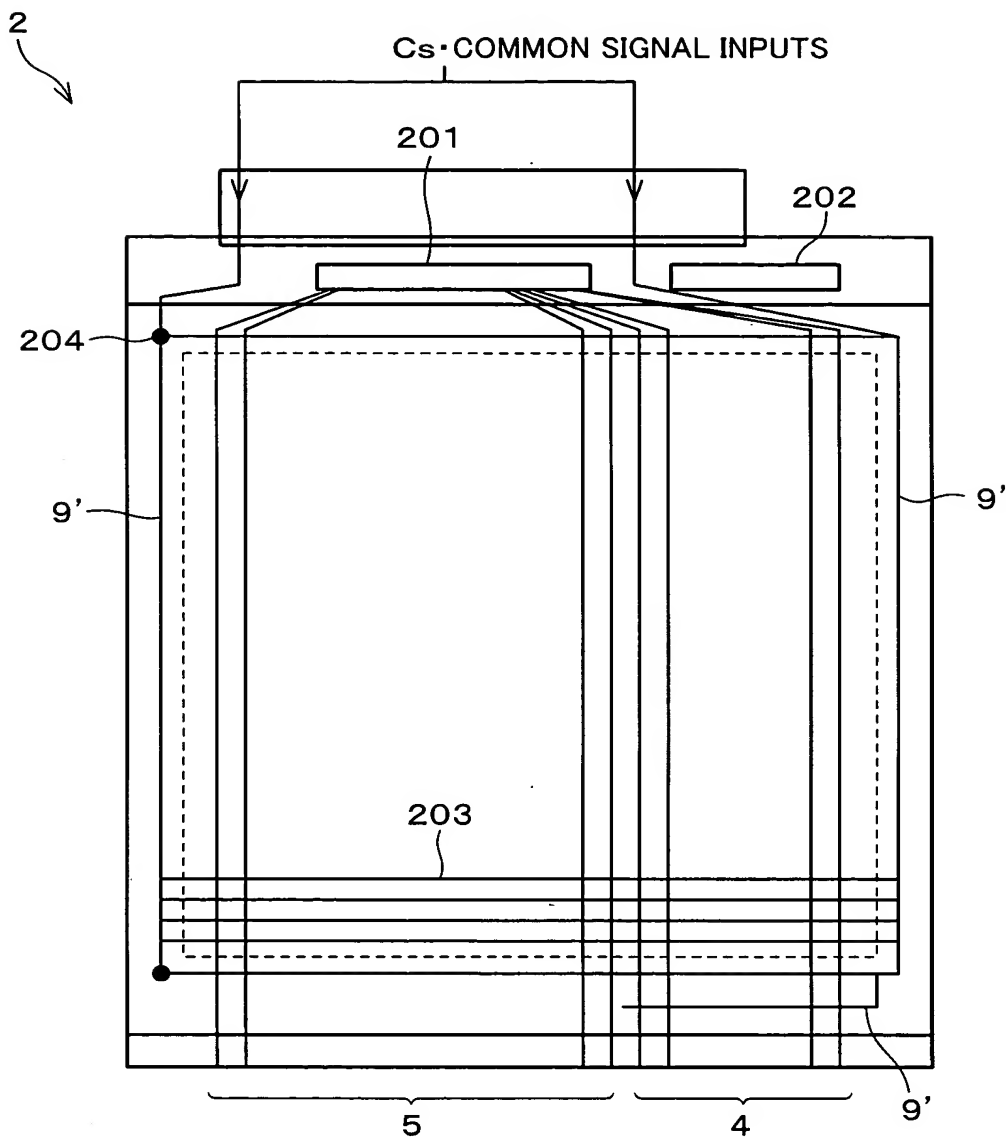


FIG. 3

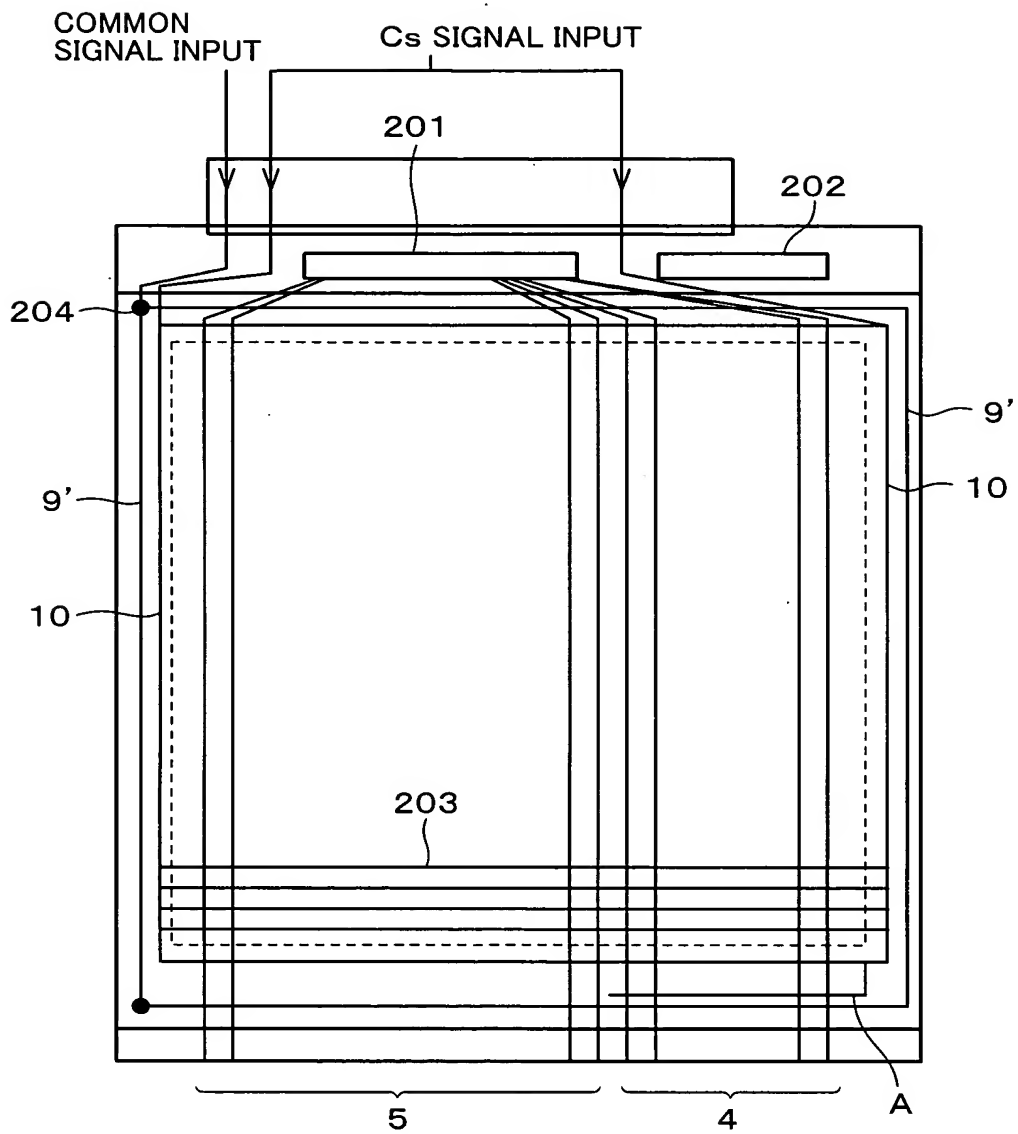


FIG. 4

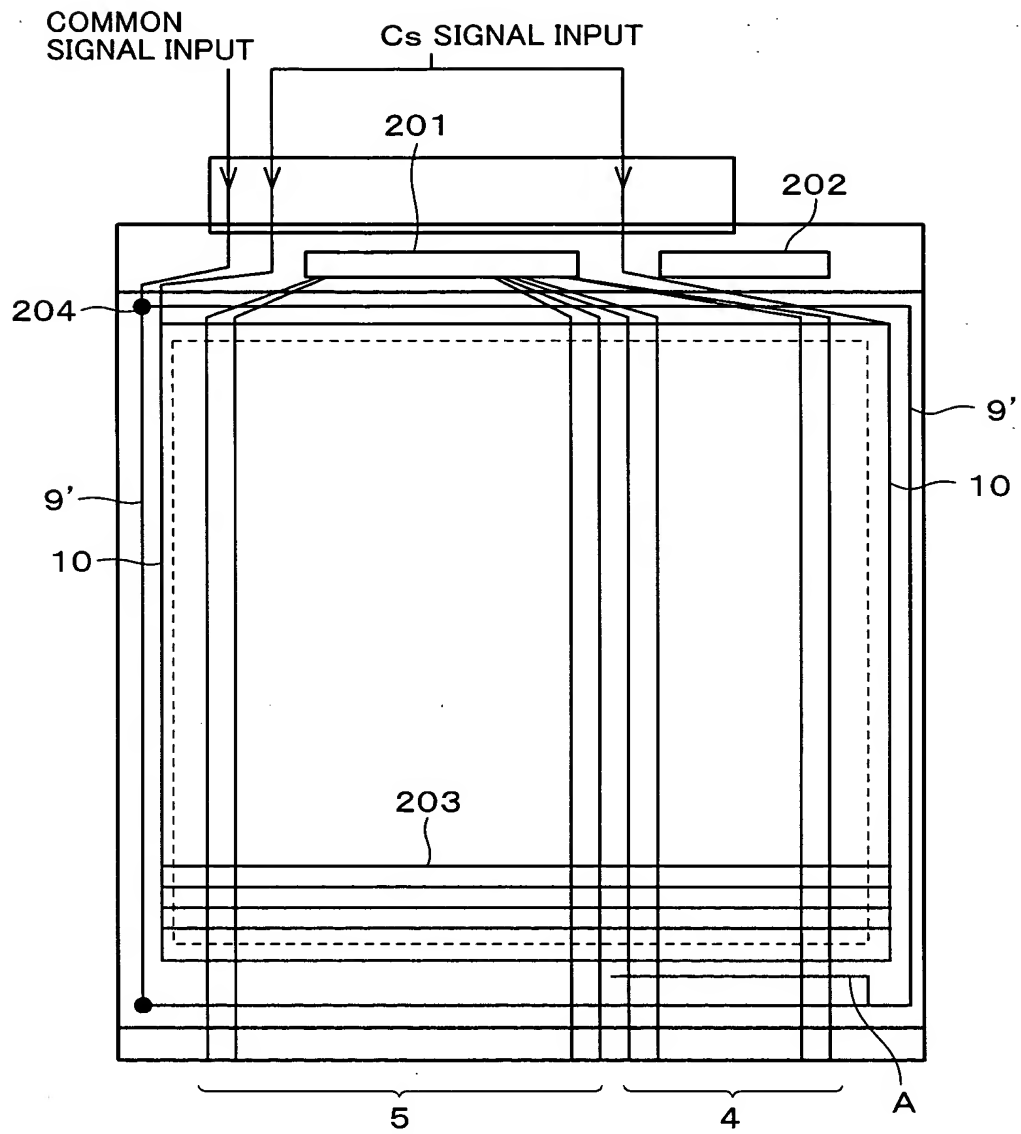


FIG. 5

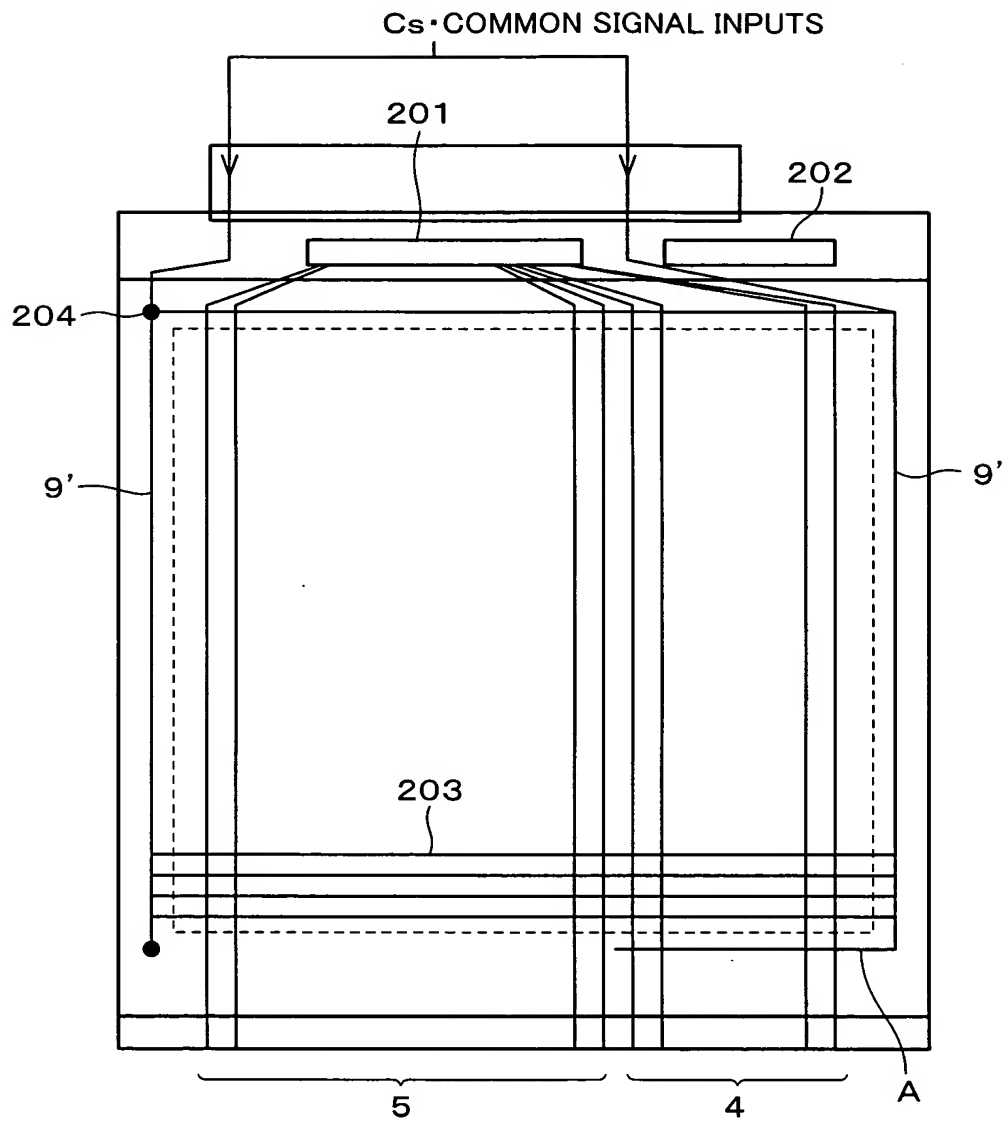


FIG. 6

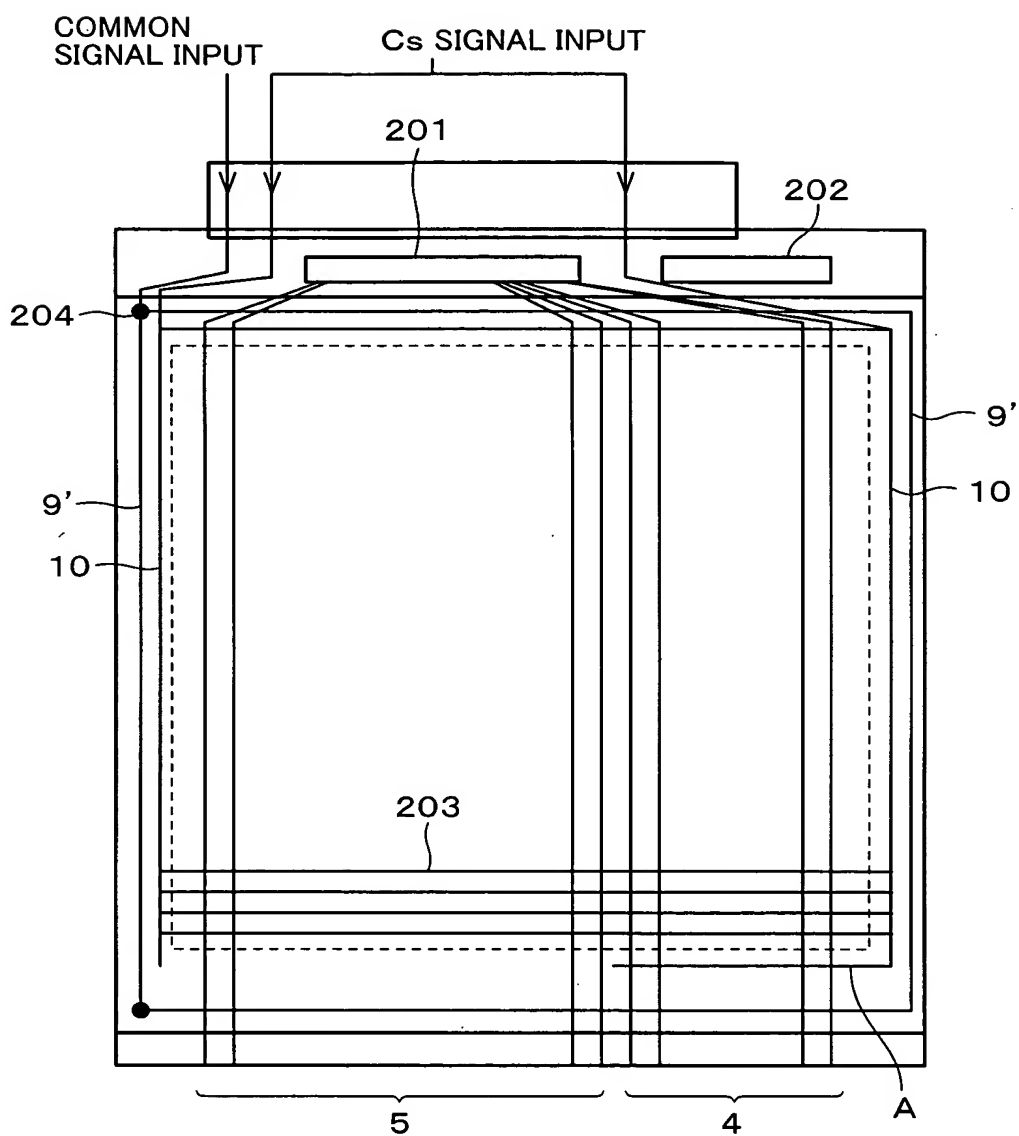


FIG. 7

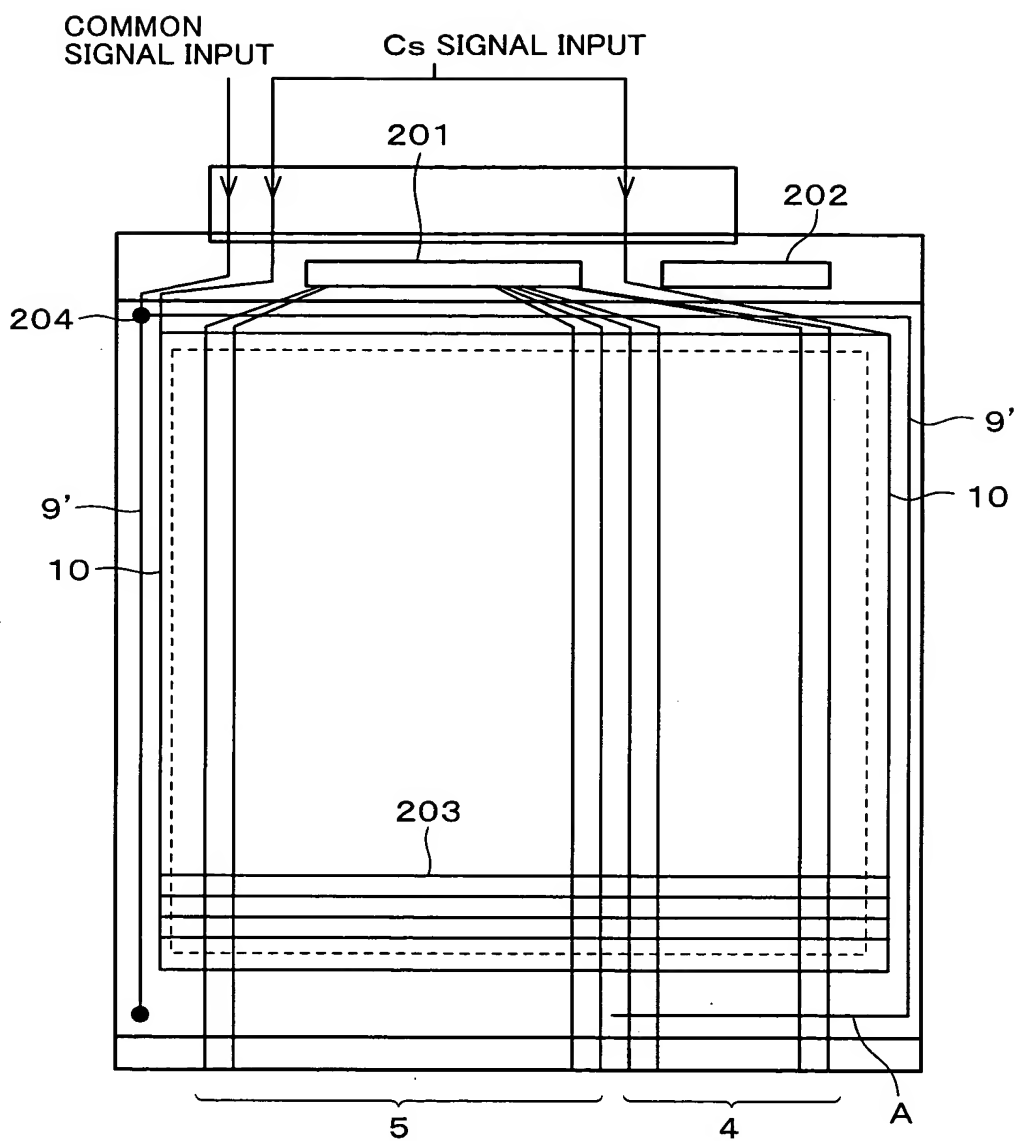


FIG. 8

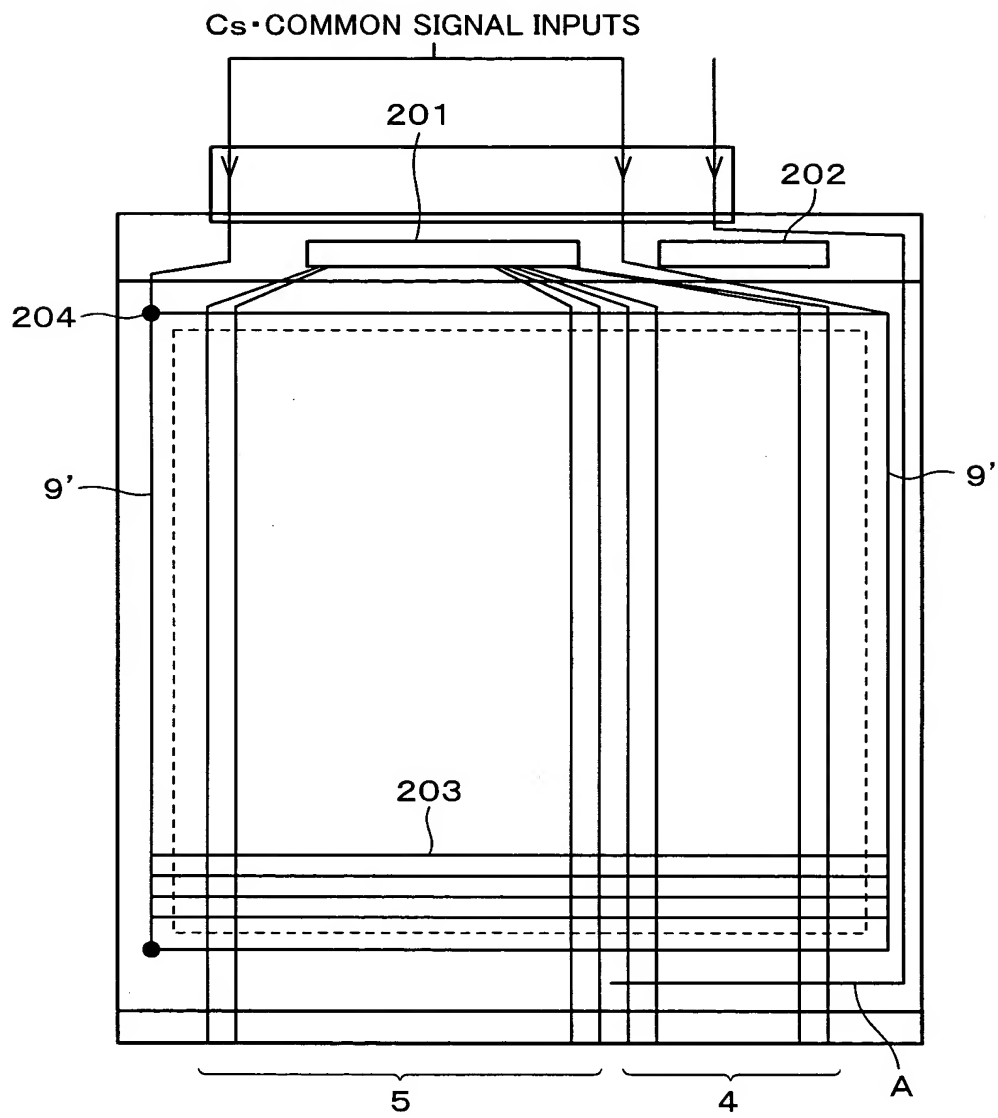




FIG. 9

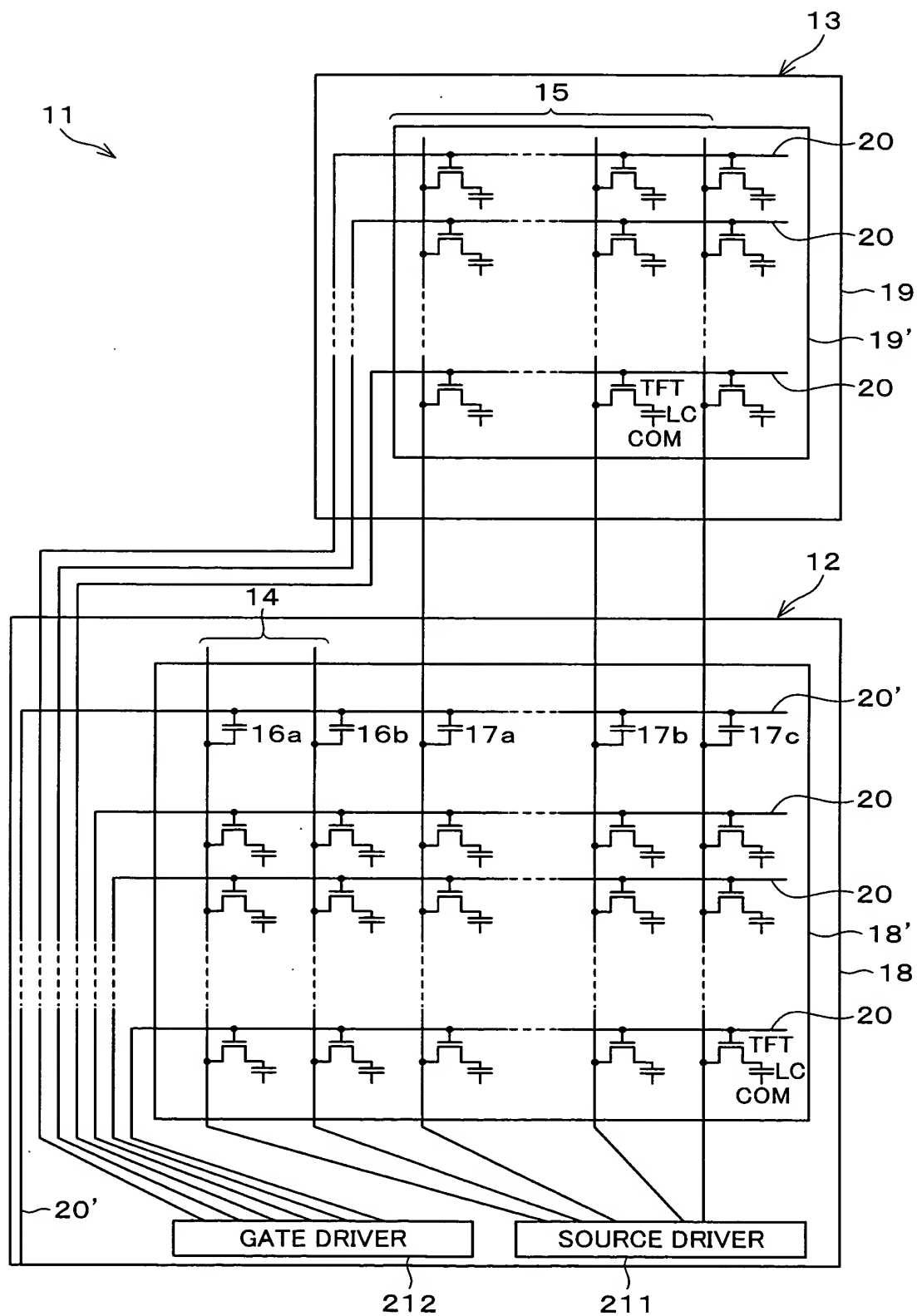
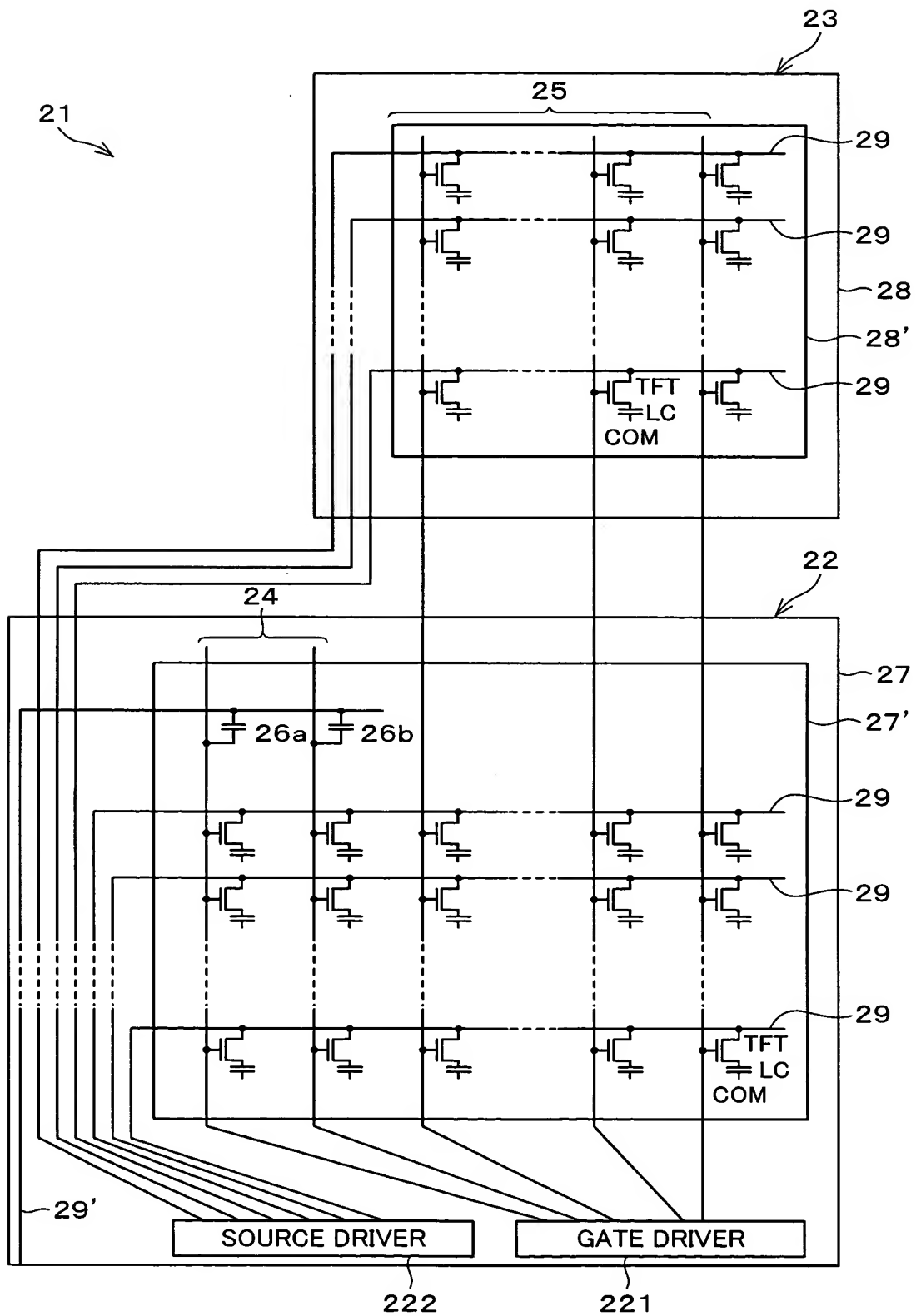


FIG. 10



The diagram illustrates a pixel array circuit. At the bottom, two driver blocks are shown: the 'SOURCE DRIVER' (labeled 232) and the 'GATE DRIVER' (labeled 231). The source driver provides signals to a series of source lines (34), which are connected to the source terminals of the TFTs in the array. The gate driver provides signals to a series of gate lines (32), which are connected to the gate terminals of the TFTs. The array itself consists of multiple rows and columns of TFTs. Each row is connected to a common gate line (38). Each column is connected to a common source line (39). The TFTs are arranged in a grid, with each TFT having a gate terminal connected to a gate line, a source terminal connected to a source line, and a drain terminal connected to a pixel electrode (40). The pixel electrodes are connected to a common data line (33). The array is divided into two main sections: a top section (35) and a bottom section (36). The top section (35) contains TFTs with gates connected to gate lines 32 and sources connected to source lines 34. The bottom section (36) contains TFTs with gates connected to gate lines 32 and sources connected to source lines 34. The pixel electrodes (40) are connected to a common data line (33). The diagram also shows various other components and connections, including a common gate line (38), a common source line (39), and a common data line (33).

FIG. 12



FIG. 13

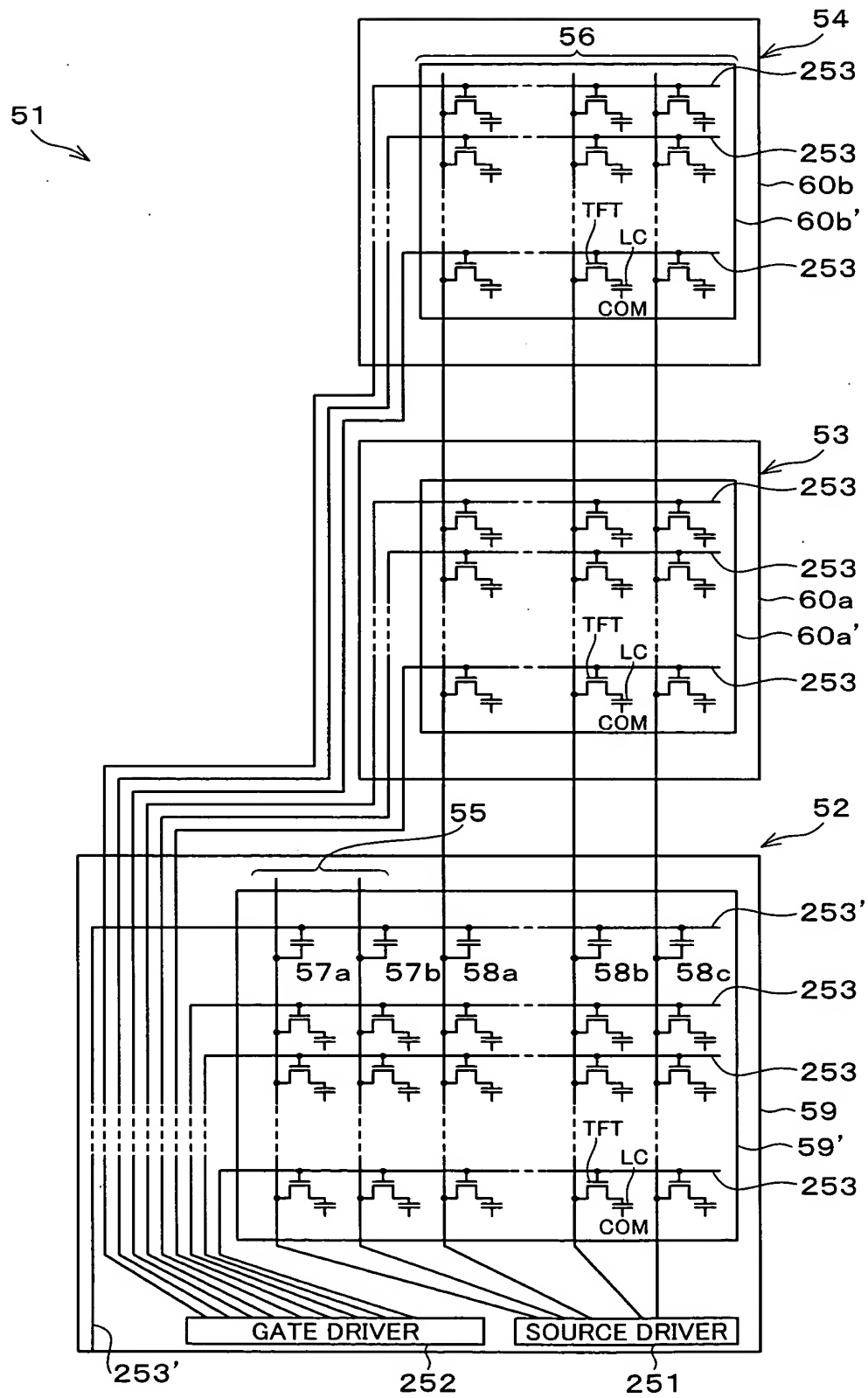


FIG. 14

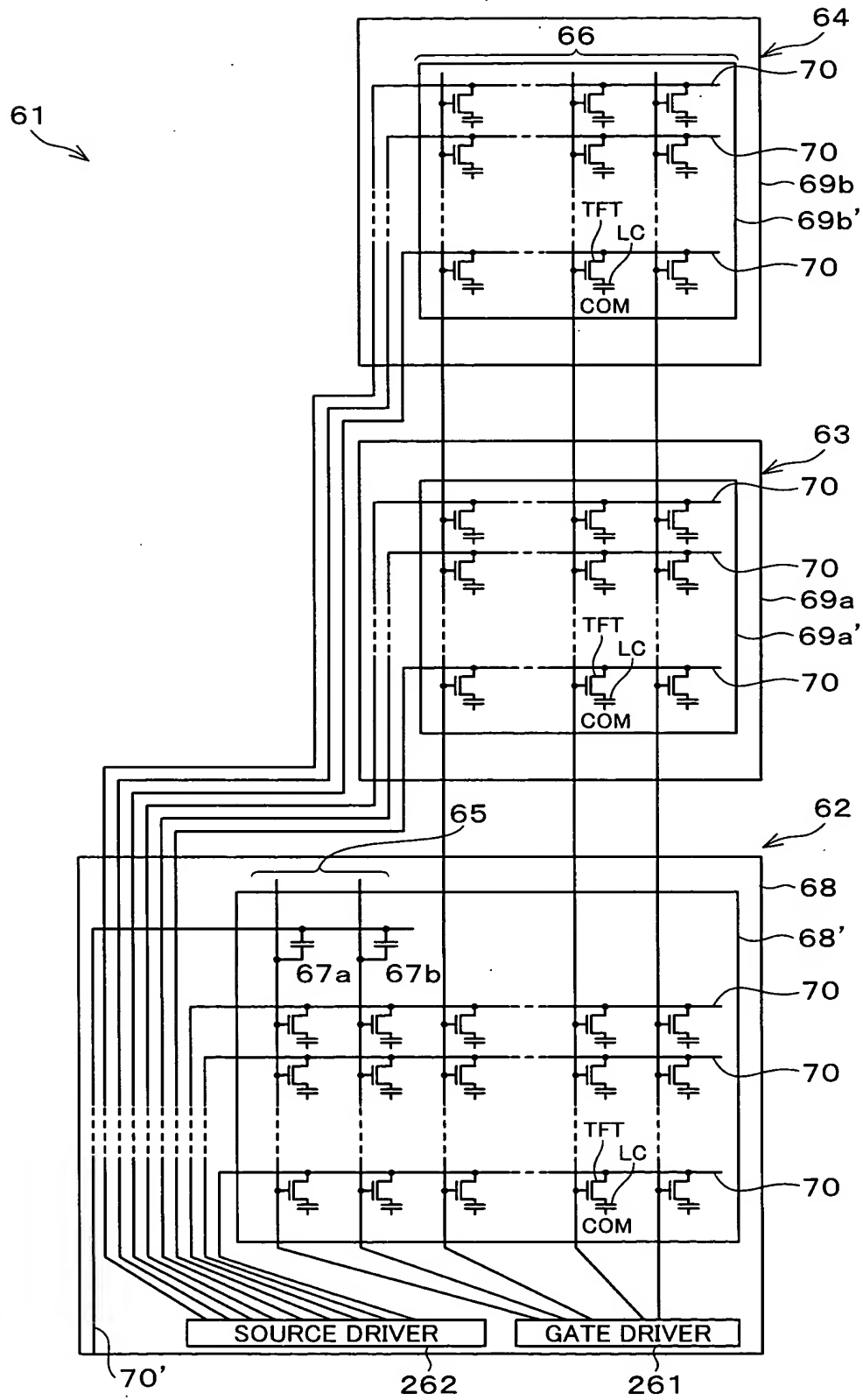


FIG. 15

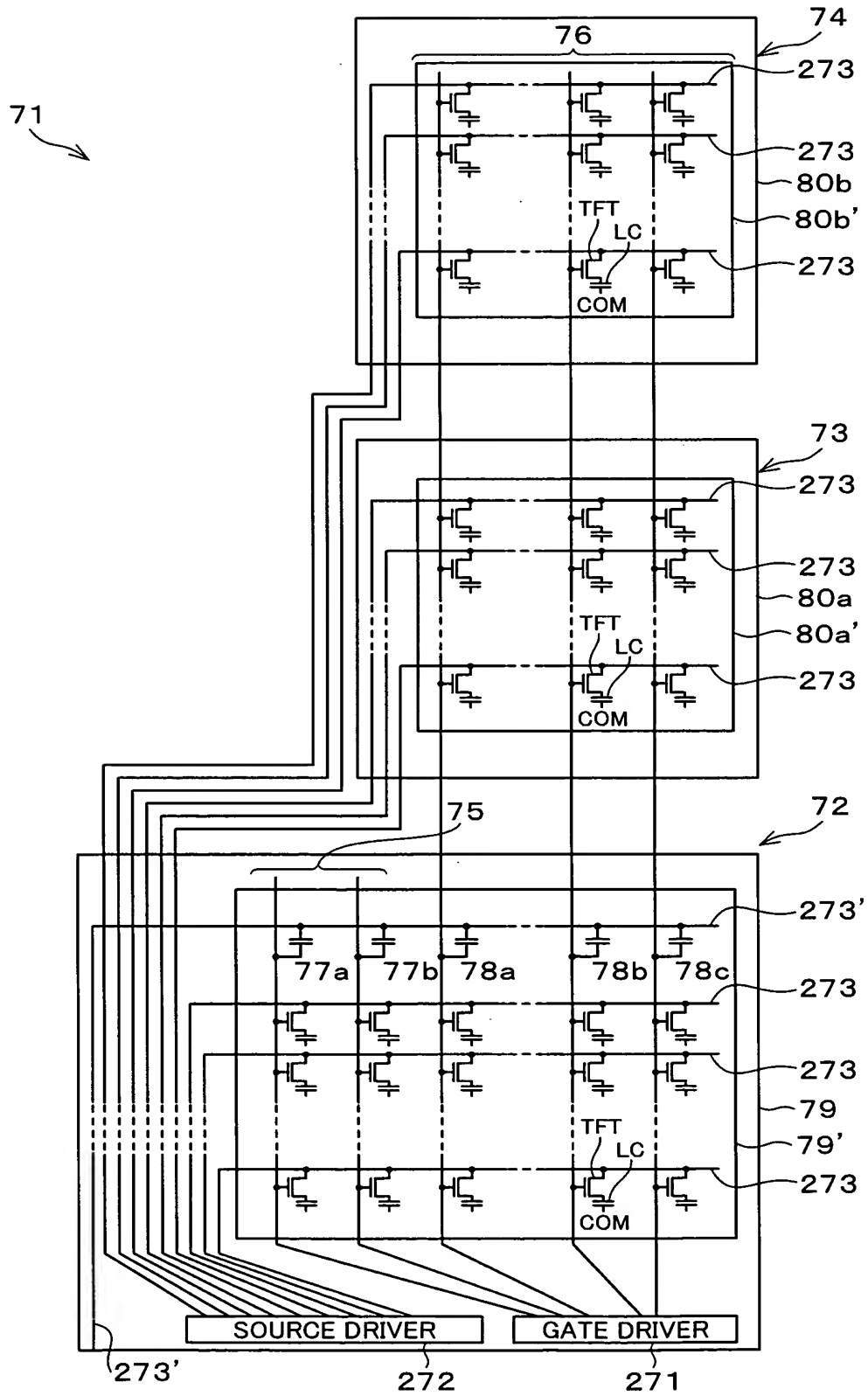


FIG. 16

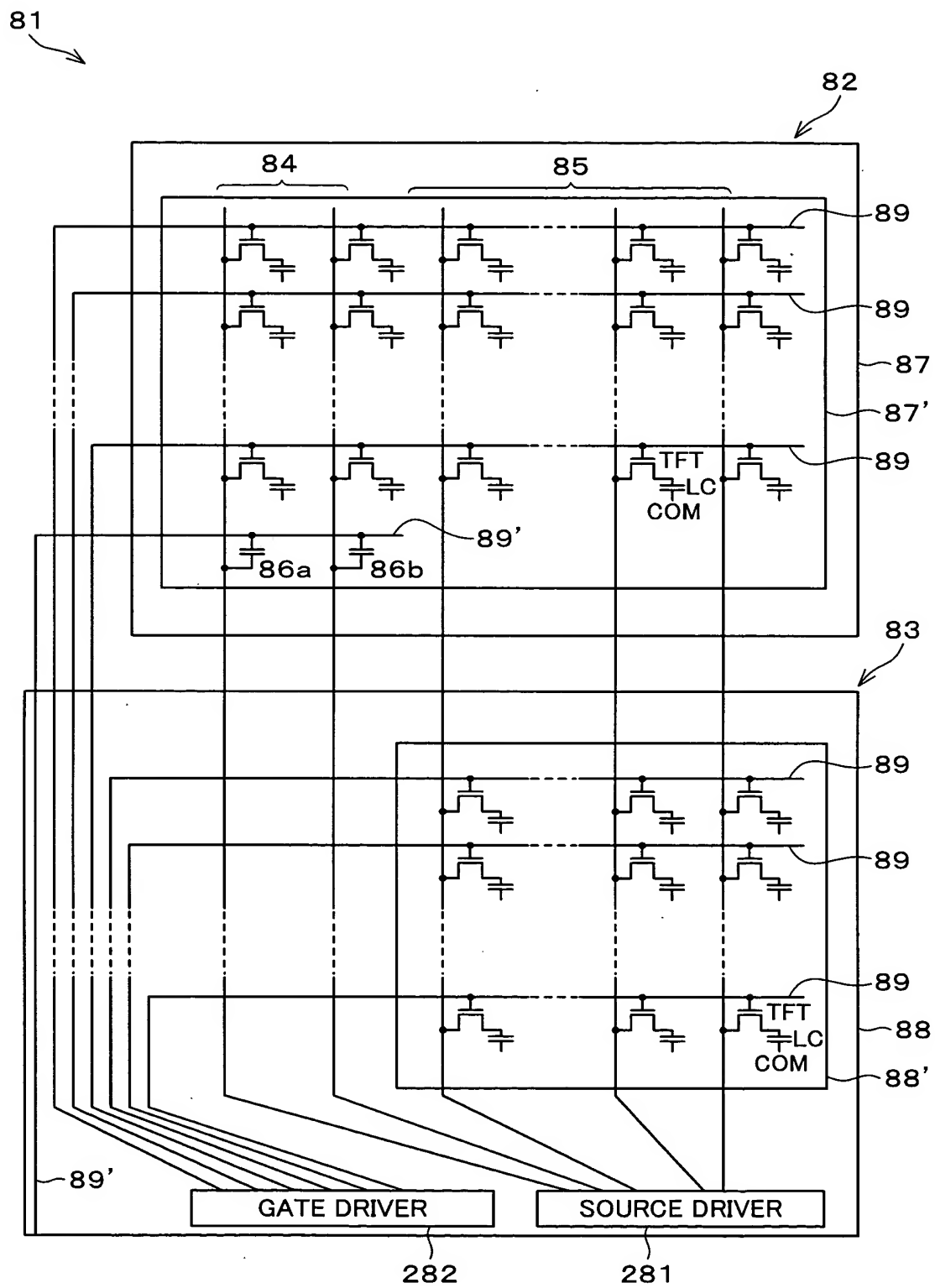




FIG. 17

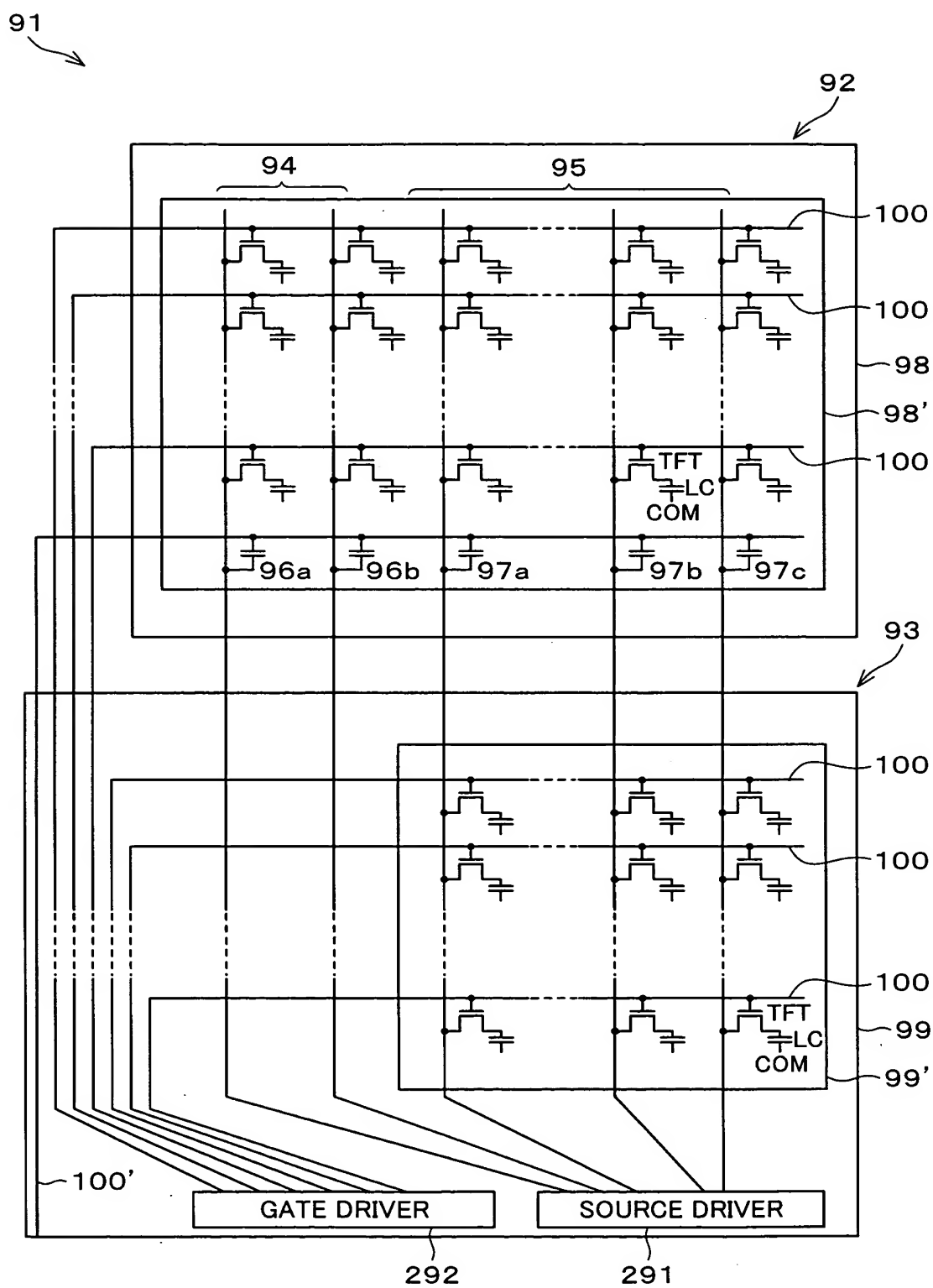


FIG. 18

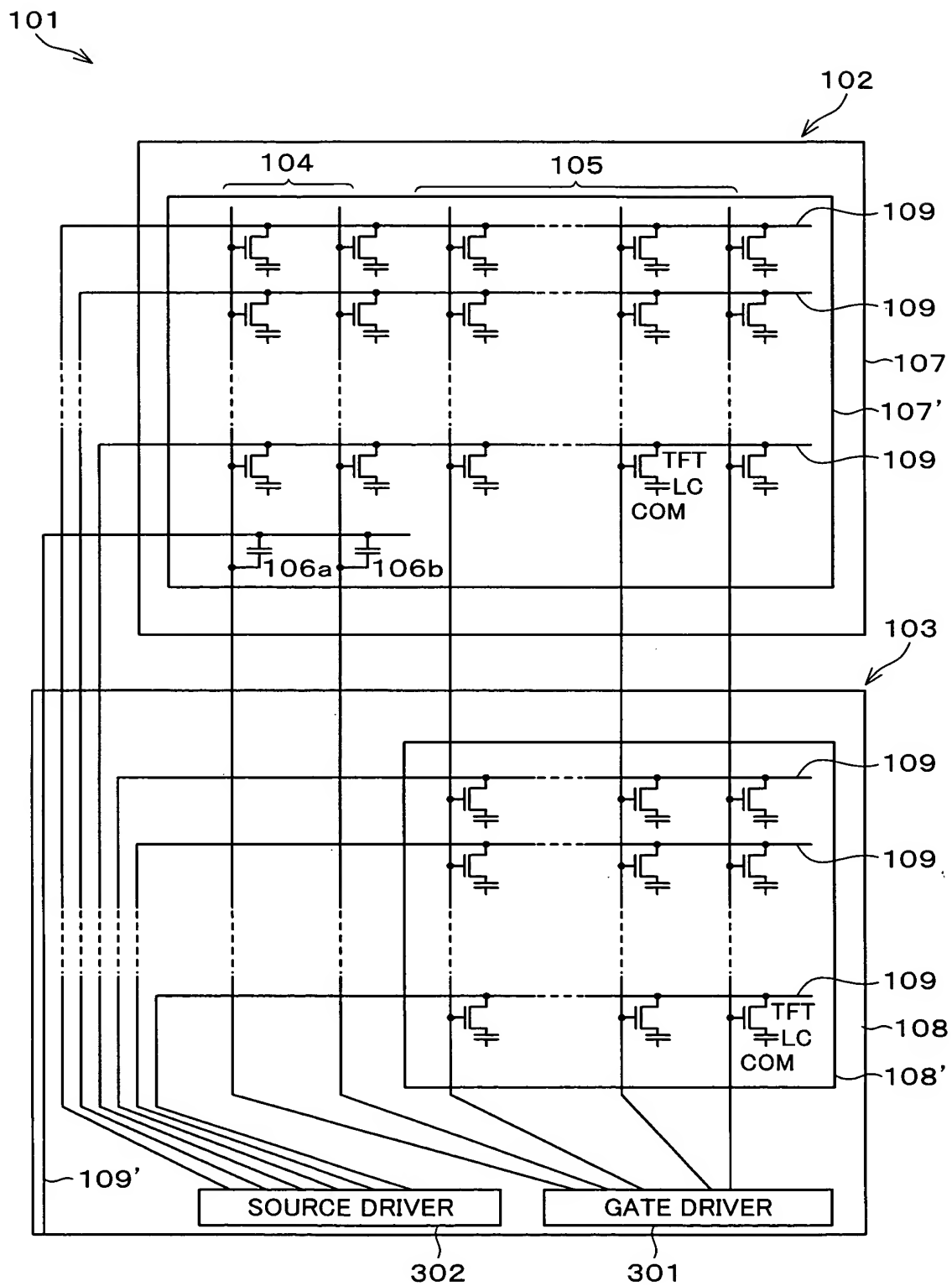


FIG. 19

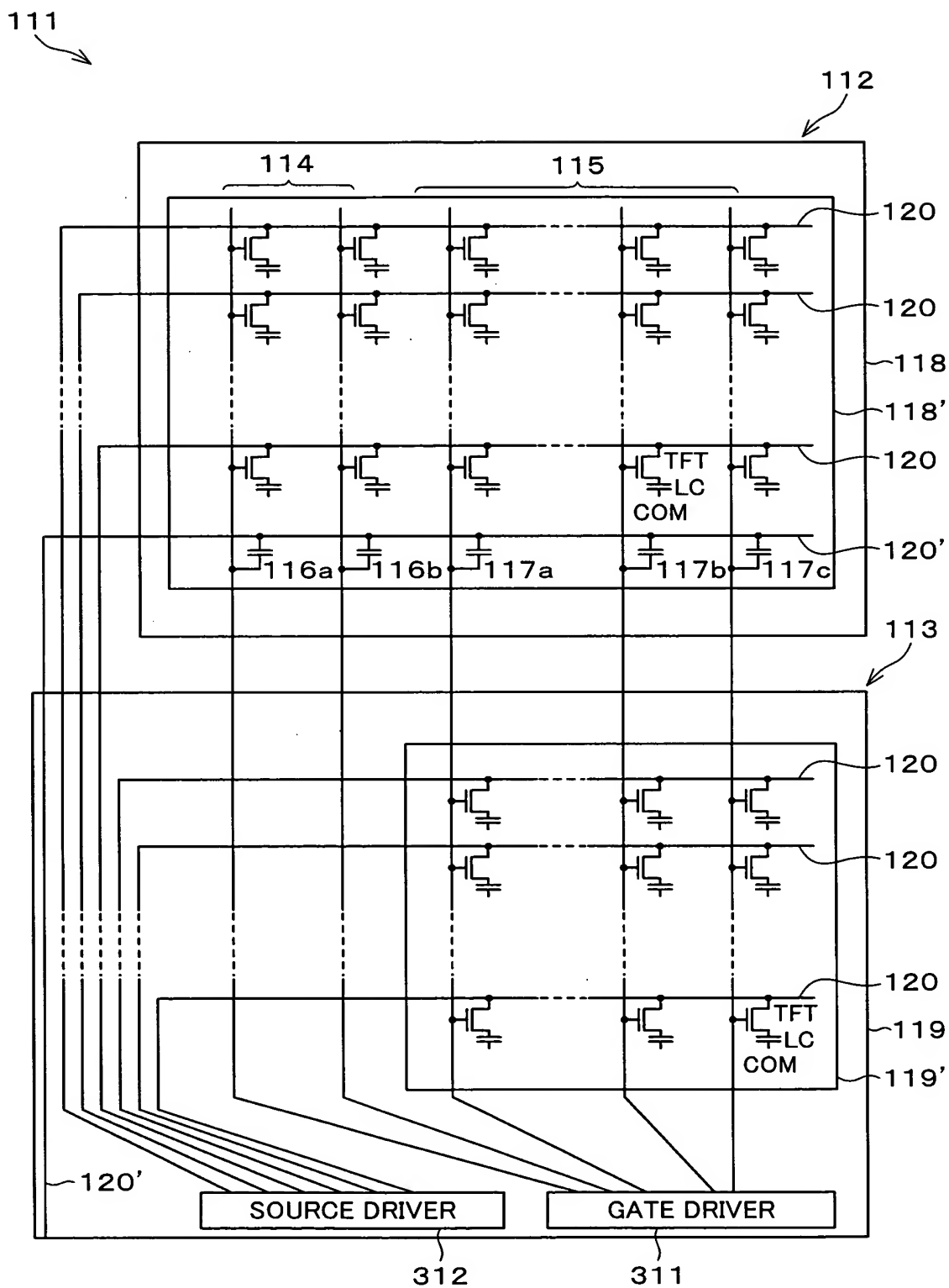
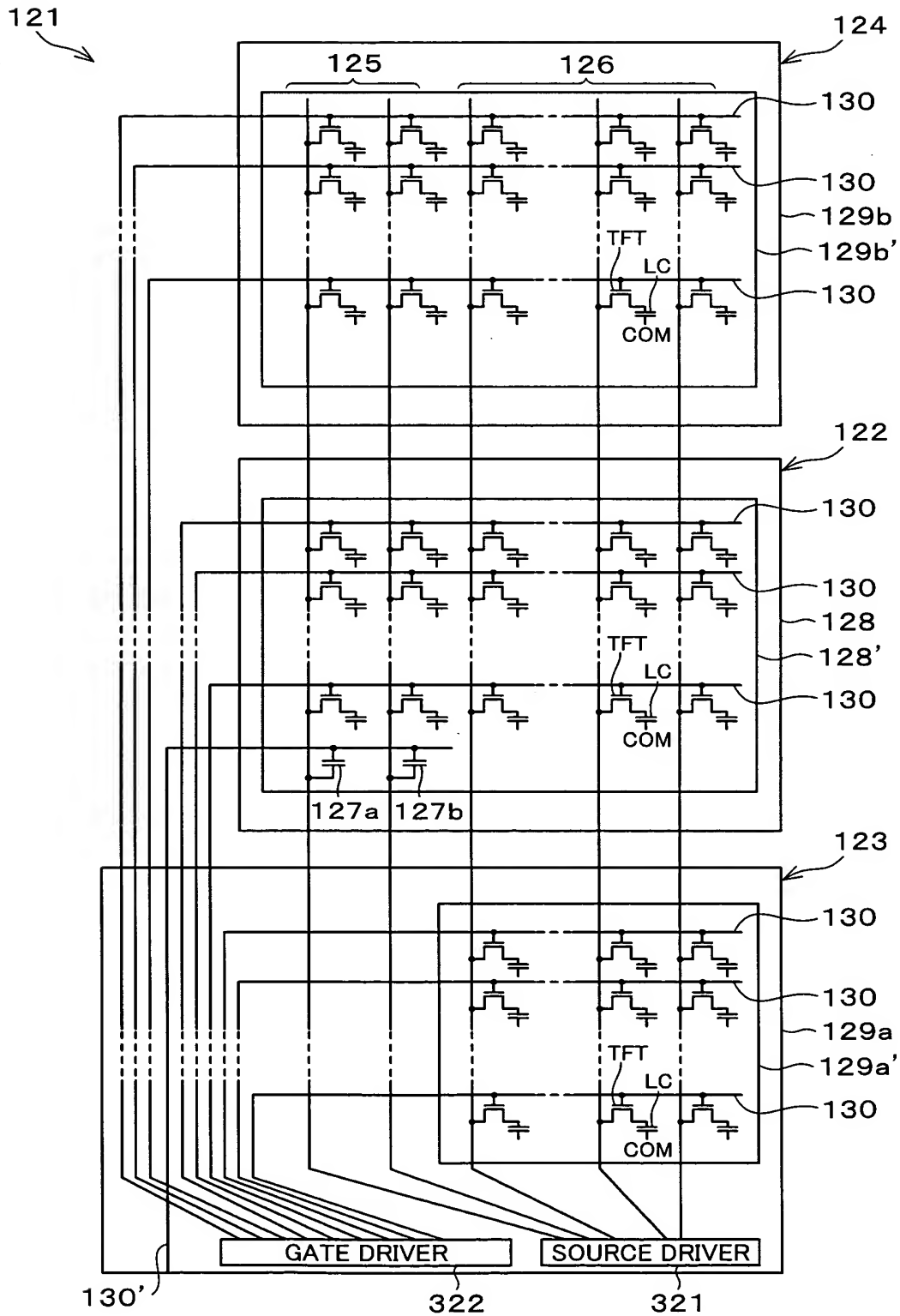


FIG. 20



# FIG. 21

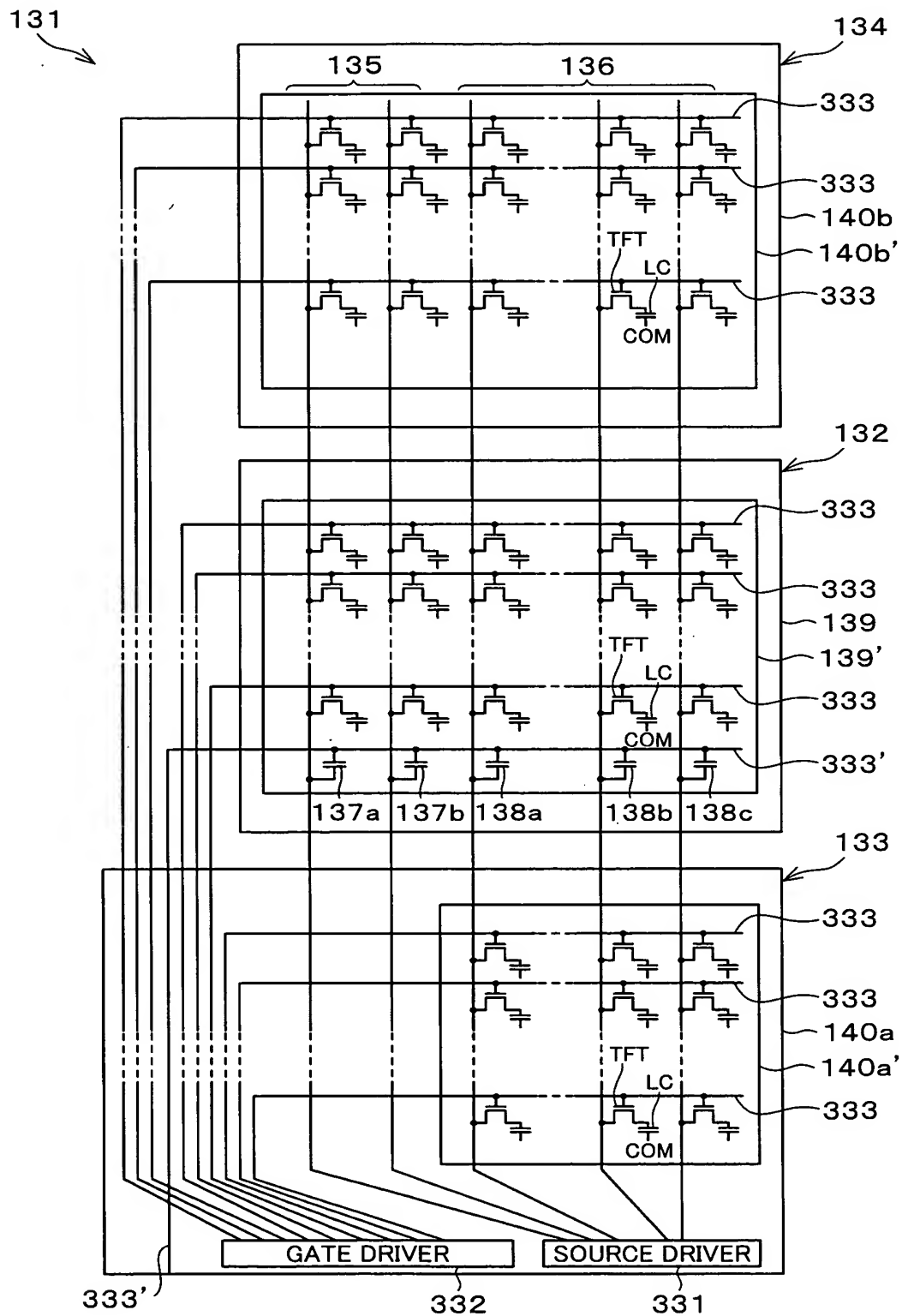
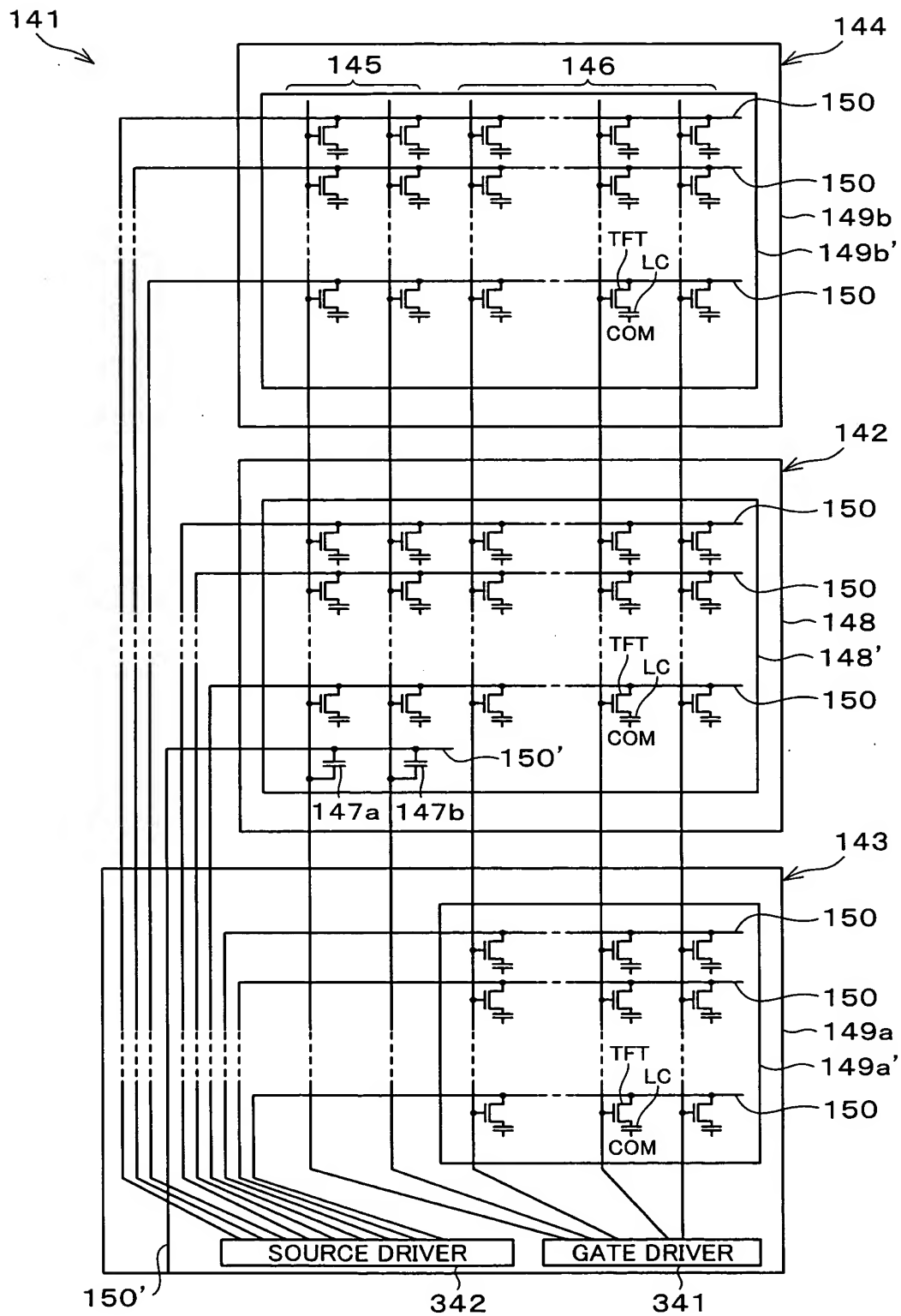


FIG. 22



# FIG. 23

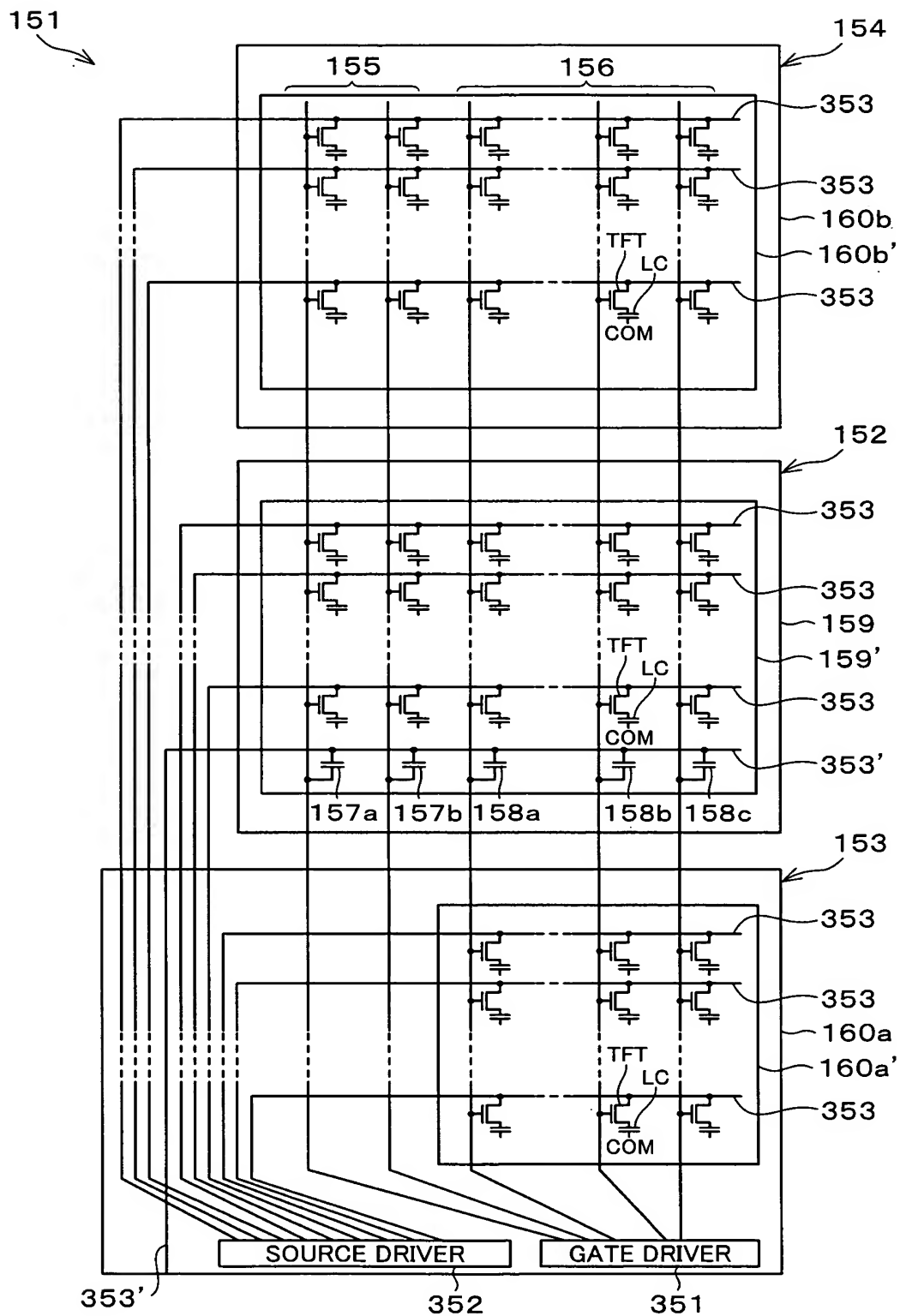


FIG. 24 (a)

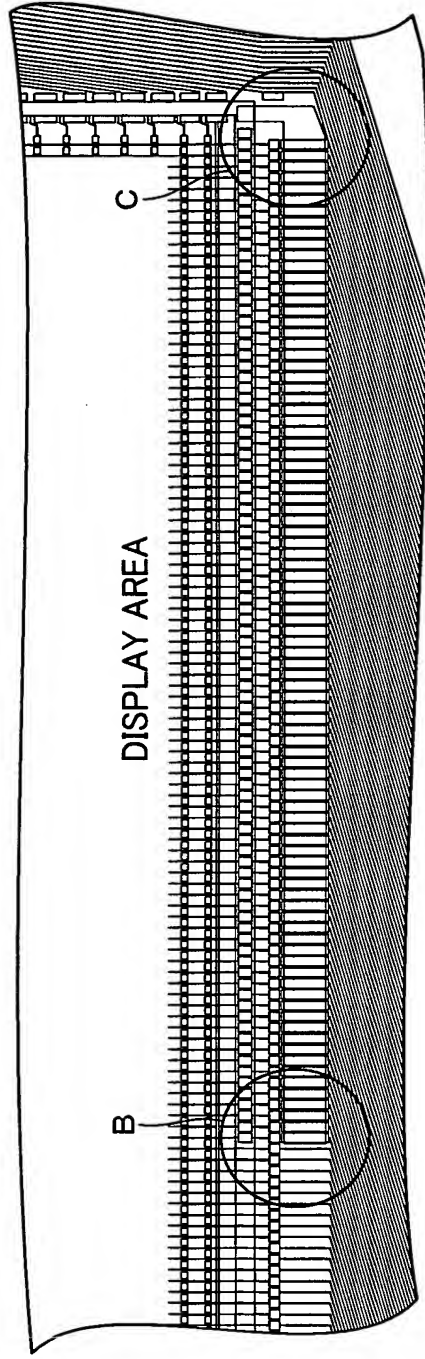


FIG. 24 (b)

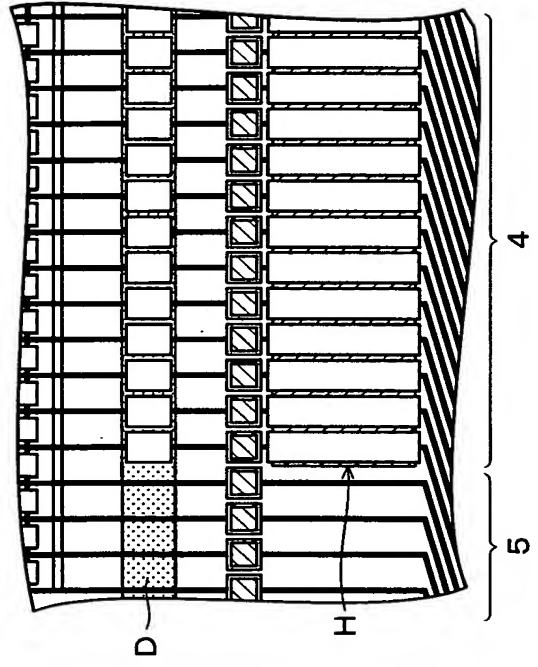


FIG. 24 (c)

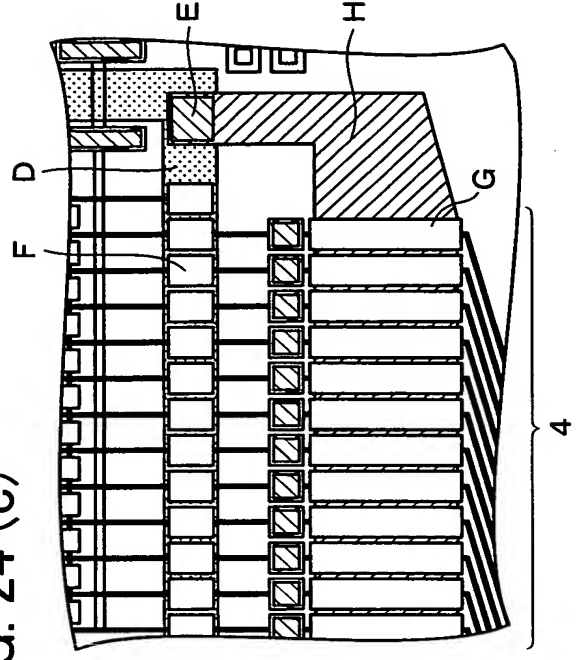




FIG. 25

